

# BLF8G09LS-270W; BLF8G09LS-270GW

Power LDMOS transistor

Rev. 3 — 1 September 2015

AMPLEON

Product data sheet

## 1. Product profile

### 1.1 General description

270 W LDMOS power transistor with improved video bandwidth for base station applications at frequencies from 716 MHz to 960 MHz.

**Table 1. Typical performance**

Typical RF performance at  $T_{case} = 25\text{ °C}$  in a common source class-AB production test circuit, tested on straight lead device.

Test signal	f (MHz)	$V_{DS}$ (V)	$P_{L(AV)}$ (W)	$G_p$ (dB)	$\eta_D$ (%)	ACPR <sub>5M</sub> (dBc)
2-carrier W-CDMA	716 to 728	28	67	20	33	-35 <sup>[1]</sup>

[1] Test signal: 3GPP test model 1; 64 DPCH; PAR = 8.4 dB at 0.01 % probability on CCDF; carrier spacing 5 MHz.

### 1.2 Features and benefits

- Excellent ruggedness
- Device can operate with the supply current delivered through the video leads
- High efficiency
- Low thermal resistance providing excellent thermal stability
- Designed for broadband operation (716 MHz to 960 MHz)
- Lower output capacitance for improved performance in Doherty applications
- Decoupling leads to enable improved video bandwidth (55 MHz typical)
- Designed for low memory effects providing excellent pre-distortability
- Internally matched for ease of use
- Integrated ESD protection
- Design optimized for gull-wing and straight lead versions
- Compliant to Directive 2002/95/EC, regarding Restriction of Hazardous Substances (RoHS)

### 1.3 Applications

- RF power amplifiers for W-CDMA base stations and multi carrier applications in the 716 MHz to 960 MHz frequency range

## 2. Pinning information

Table 2. Pinning

Pin	Description	Simplified outline	Graphic symbol
<b>BLF8G09LS-270W (SOT1244B)</b>			
1	drain		<p>aaa-003619</p>
2	gate		
3	source		
4	video lead		
5	video lead		
6	n.c.		
7	n.c.		
<b>BLF8G09LS-270GW (SOT1244C)</b>			
1	drain		<p>aaa-003619</p>
2	gate		
3	source		
4	video lead		
5	video lead		
6	n.c.		
7	n.c.		

[1] Connected to flange.

[2] Device can operate with the supply current delivered through the combined video leads.

## 3. Ordering information

Table 3. Ordering information

Type number	Package		
	Name	Description	Version
BLF8G09LS-270W	-	earless flanged ceramic package; 6 leads	SOT1244B
BLF8G09LS-270GW	-	earless flanged ceramic package; 6 leads	SOT1244C

## 4. Limiting values

Table 4. Limiting values

In accordance with the Absolute Maximum Rating System (IEC 60134).

Symbol	Parameter	Conditions	Min	Max	Unit
$V_{DS}$	drain-source voltage		-	65	V
$V_{GS}$	gate-source voltage		-0.5	+13	V
$T_{stg}$	storage temperature		-65	+150	°C
$T_j$	junction temperature	[1]	-	225	°C

[1] Continuous use at maximum temperature will affect the reliability, for details refer to the on-line MTF calculator.

## 5. Thermal characteristics

**Table 5. Thermal characteristics**

Symbol	Parameter	Conditions	Typ	Unit
$R_{th(j-c)}$	thermal resistance from junction to case	$T_{case} = 80\text{ °C}; P_L = 67\text{ W}; V_{DS} = 28\text{ V}; I_{Dq} = 2000\text{ mA}$	0.265	K/W

## 6. Characteristics

**Table 6. DC characteristics**

$T_j = 25\text{ °C};$  unless otherwise specified.

Symbol	Parameter	Conditions	Min	Typ	Max	Unit
$V_{(BR)DSS}$	drain-source breakdown voltage	$V_{GS} = 0\text{ V}; I_D = 4.5\text{ mA}$	65	-	-	V
$V_{GS(th)}$	gate-source threshold voltage	$V_{DS} = 10\text{ V}; I_D = 450\text{ mA}$	1.5	1.8	2.3	V
$I_{DSS}$	drain leakage current	$V_{GS} = 0\text{ V}; V_{DS} = 28\text{ V}$	-	-	4.2	$\mu\text{A}$
$I_{DSX}$	drain cut-off current	$V_{GS} = V_{GS(th)} + 3.75\text{ V}; V_{DS} = 10\text{ V}$	-	82	-	A
$I_{GSS}$	gate leakage current	$V_{GS} = 11\text{ V}; V_{DS} = 0\text{ V}$	-	-	420	nA
$g_{fs}$	forward transconductance	$V_{DS} = 10\text{ V}; I_D = 450\text{ mA}$	-	3.92	-	S
$R_{DS(on)}$	drain-source on-state resistance	$V_{GS} = V_{GS(th)} + 3.75\text{ V}; I_D = 15.75\text{ A}$	-	0.04	-	$\Omega$

**Table 7. RF characteristics**

Test signal: 2-carrier W-CDMA; PAR = 8.4 dB at 0.01 % probability on the CCDF; 3GPP test model 1; 1-64 DPCH;  $f_1 = 718.5\text{ MHz}; f_2 = 723.5\text{ MHz}; f_3 = 720.5\text{ MHz}; f_4 = 725.5\text{ MHz};$  RF performance at  $V_{DS} = 28\text{ V}; I_{Dq} = 2000\text{ mA}; T_{case} = 25\text{ °C};$  unless otherwise specified; in a class-AB production test circuit, tested on straight lead device.

Symbol	Parameter	Conditions	Min	Typ	Max	Unit
$G_p$	power gain	$P_{L(AV)} = 67\text{ W}$	18.8	20	-	dB
$RL_{in}$	input return loss	$P_{L(AV)} = 67\text{ W}$	-	-13	-9	dB
$\eta_D$	drain efficiency	$P_{L(AV)} = 67\text{ W}$	28	33	-	%
$ACPR_{5M}$	adjacent channel power ratio (5 MHz)	$P_{L(AV)} = 67\text{ W}$	-	-35	-30	dBc

## 7. Test information

### 7.1 Ruggedness in class-AB operation

The BLF8G09LS-270W and BLF8G09LS-270GW are capable of withstanding a load mismatch corresponding to VSWR = 7 : 1 through all phases under the following conditions: PAR = 8.4 dB at 0.01 % probability on CCDF; 3GPP test model 1; 64 DPCH;  $V_{DS} = 28\text{ V}; I_{Dq} = 2000\text{ mA};$  2-carrier W-CDMA signal;  $P_{L(AV)} = 51.8\text{ dBm}; f = 716\text{ MHz}.$

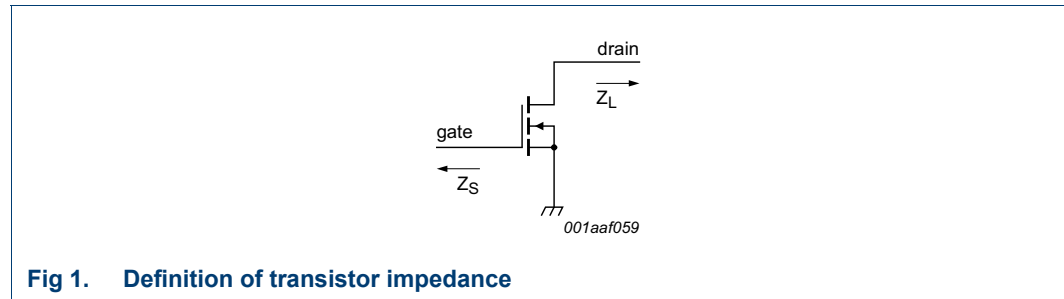
## 7.2 Impedance information

**Table 8. Typical impedance**

Measured load-pull data;  $I_{Dq} = 2000 \text{ mA}$ ;  $V_{DS} = 28 \text{ V}$ . Typical values unless otherwise specified.

f (MHz)	$Z_S$ <sup>[1]</sup> ( $\Omega$ )	$Z_L$ <sup>[1]</sup> ( $\Omega$ )
716	1.09 – j1.62	2.31 – j1.69
800	1.43 – j2.41	2.06 – j0.76
869	1.46 – j3.21	1.6 – j0.66
925	1.88 – j3.62	1.23 – j0.39
960	2.22 – j4.73	1.01 – j0.55

[1]  $Z_S$  and  $Z_L$  defined in [Figure 1](#).

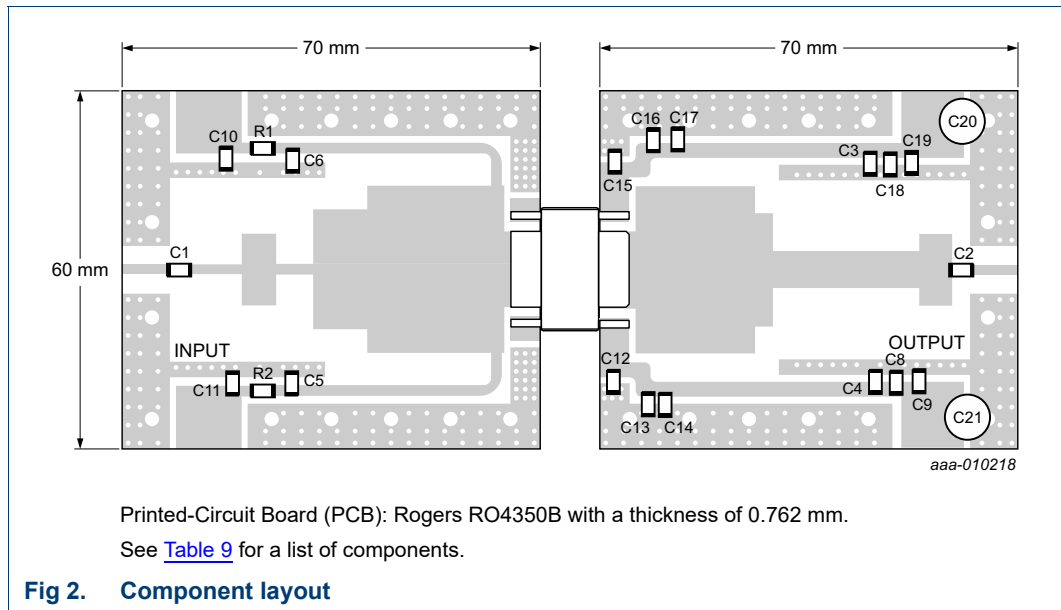


**Fig 1. Definition of transistor impedance**

## 7.3 VBW in class-AB operation

The BLF8G09LS-270W and BLF8G09LS-270GW show 55 MHz (typical) video bandwidth in class-AB test circuit in 722 MHz band at  $V_{DS} = 28 \text{ V}$  and  $I_{Dq} = 2000 \text{ mA}$ .

7.4 Test circuit



**Table 9. List of components**

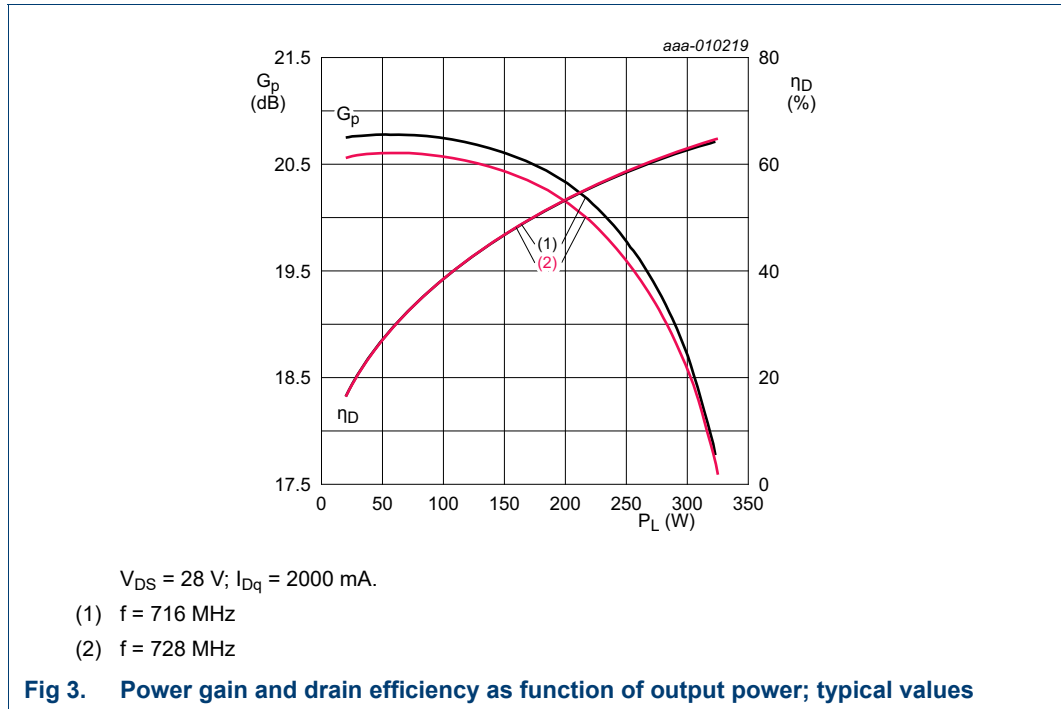
For test circuit see [Figure 2](#).

Component	Description	Value	Remarks
C1, C2, C3, C4, C5, C6	multilayer ceramic chip capacitor	510 pF	ATC100B
C8, C9, C10, C11, C12, C13, C14, C15, C16, C17, C18, C19	multilayer ceramic chip capacitor	4.7 $\mu$ F, 50 V	Murata
C20, C21	electrolytic capacitor	2200 $\mu$ F, 63 V	
R1, R2	resistor	9.1 $\Omega$	SMD 0805

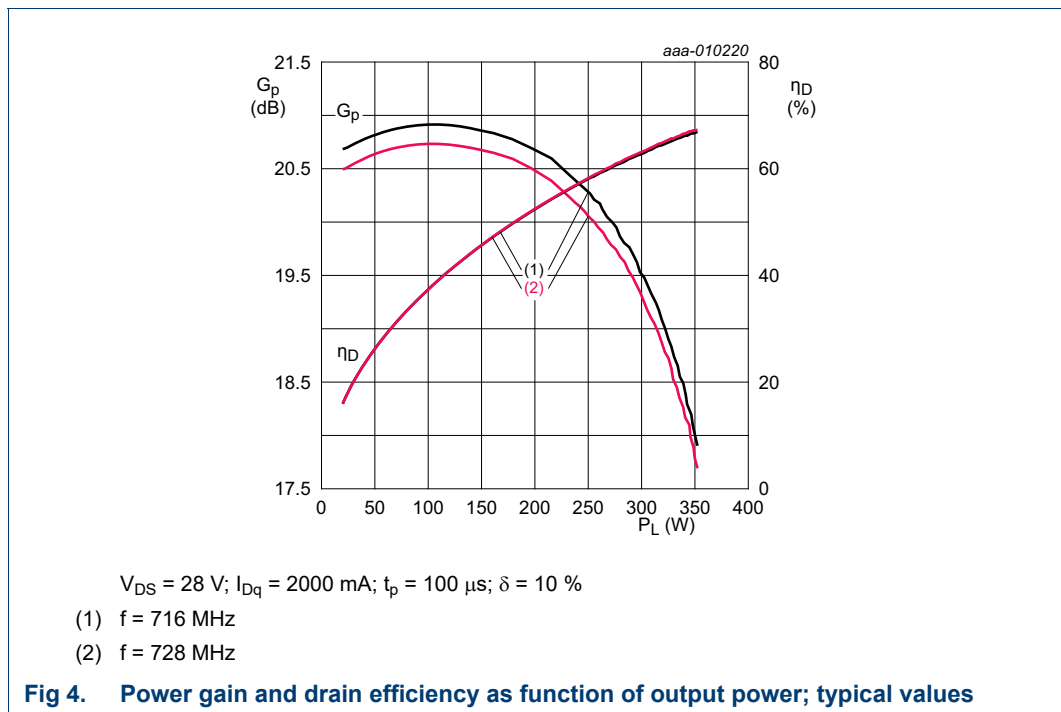
7.5 Graphical data

7.5.1 Straight lead

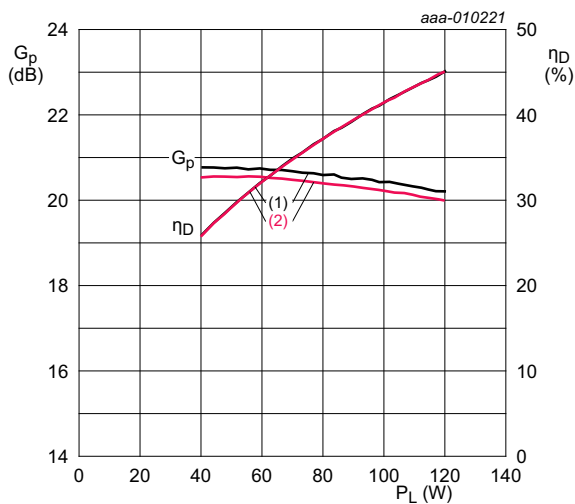
7.5.1.1 CW



7.5.1.2 CW pulsed

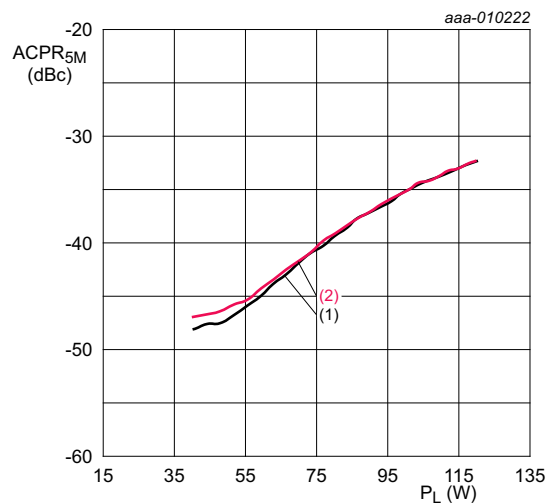


7.5.1.3 1-Carrier W-CDMA



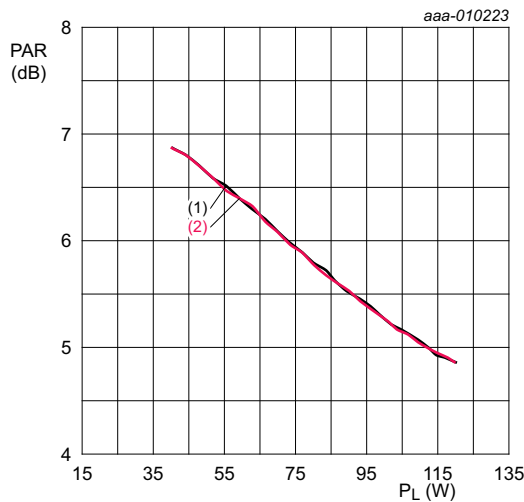
$V_{DS} = 28\text{ V}; I_{Dq} = 2000\text{ mA}$ .  
 (1)  $f = 716\text{ MHz}$   
 (2)  $f = 728\text{ MHz}$

Fig 5. Power gain and drain efficiency as function of output power; typical values



$V_{DS} = 28\text{ V}; I_{Dq} = 2000\text{ mA}$ .  
 (1)  $f = 716\text{ MHz}$   
 (2)  $f = 728\text{ MHz}$

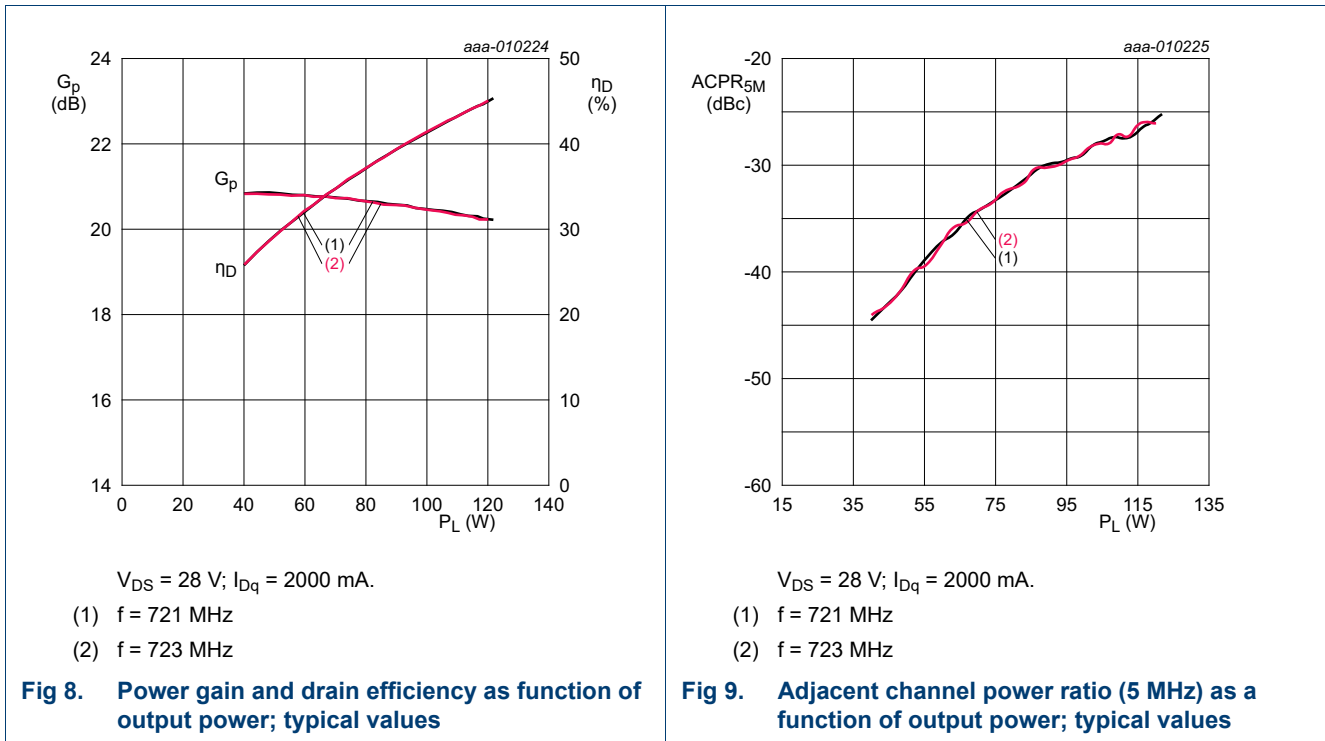
Fig 6. Adjacent channel power ratio (5 MHz) as a function of output power; typical values



$V_{DS} = 28\text{ V}; I_{Dq} = 2000\text{ mA}$ .  
 (1)  $f = 716\text{ MHz}$   
 (2)  $f = 728\text{ MHz}$

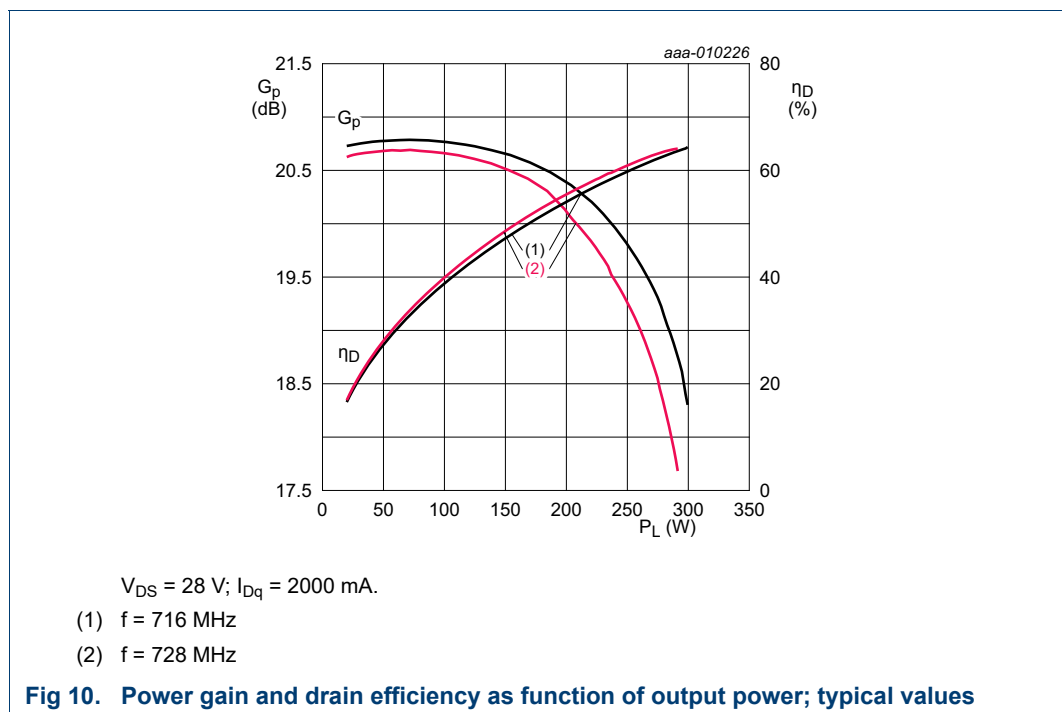
Fig 7. Peak-to-average ratio as a function of output power; typical values

7.5.1.4 2-Carrier W-CDMA



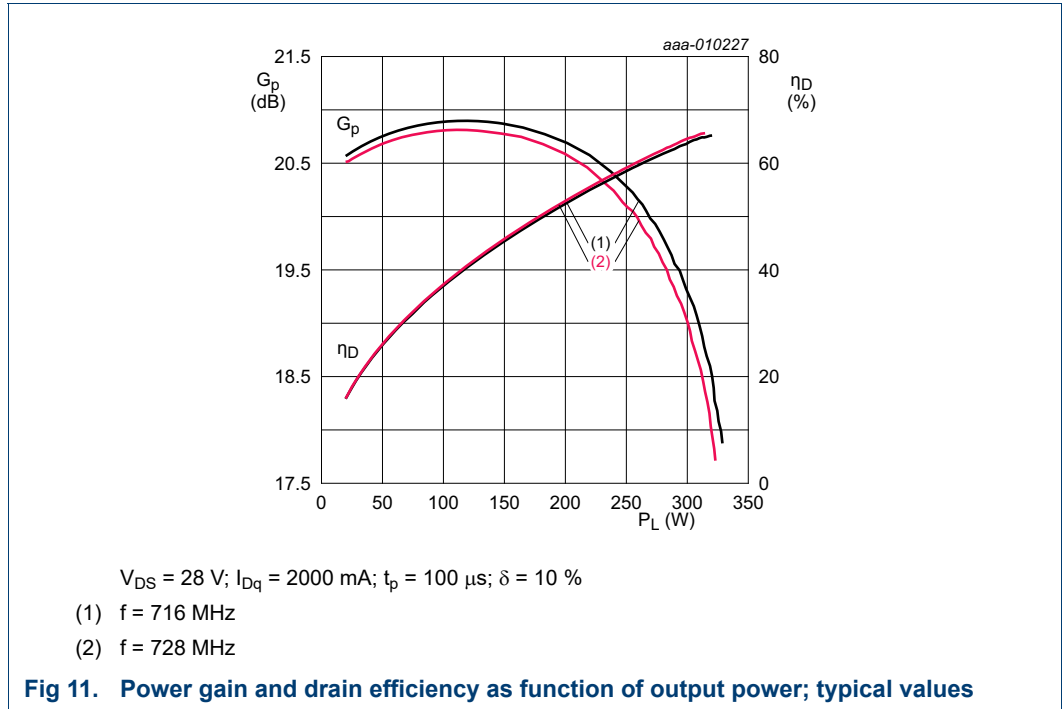
7.5.2 Gull-wing

7.5.2.1 CW

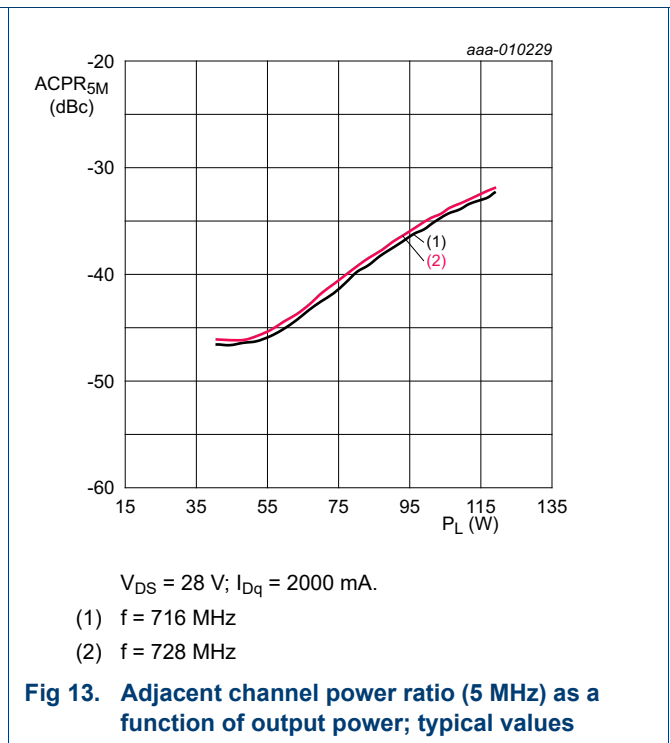
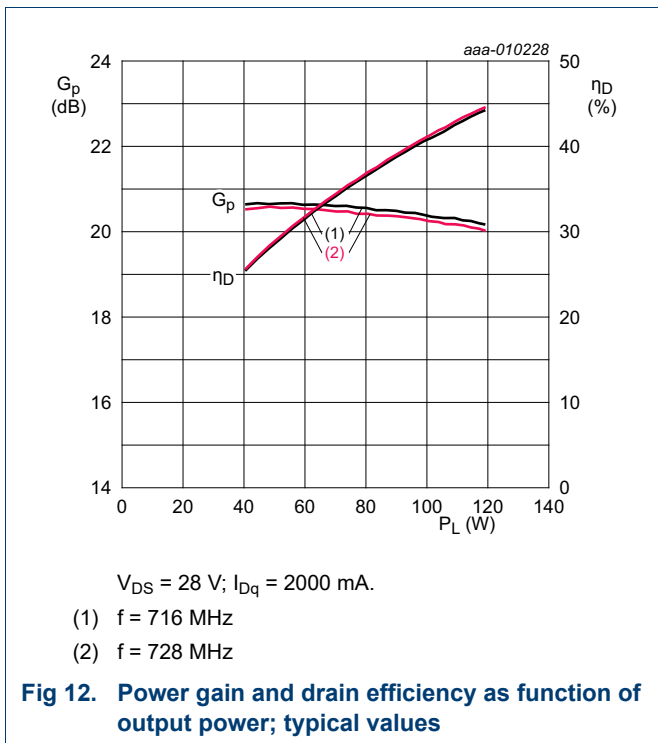


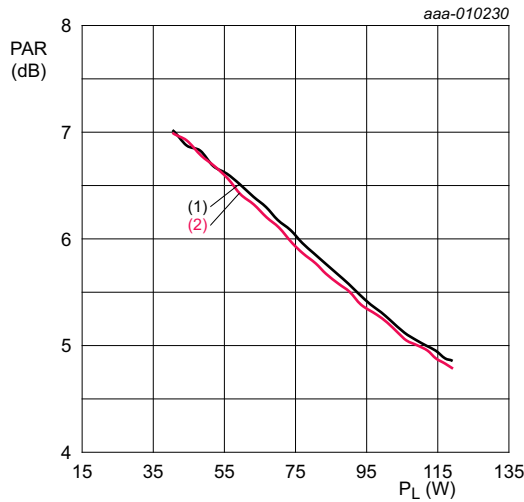


7.5.2.2 CW pulsed



7.5.2.3 1-Carrier W-CDMA

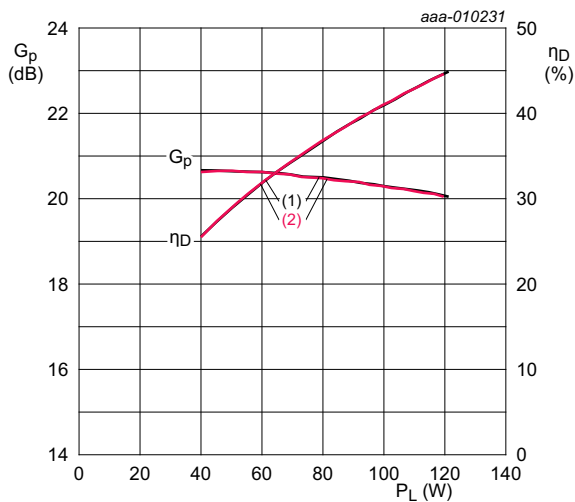




$V_{DS} = 28\text{ V}; I_{Dq} = 2000\text{ mA}$ .  
 (1)  $f = 716\text{ MHz}$   
 (2)  $f = 728\text{ MHz}$

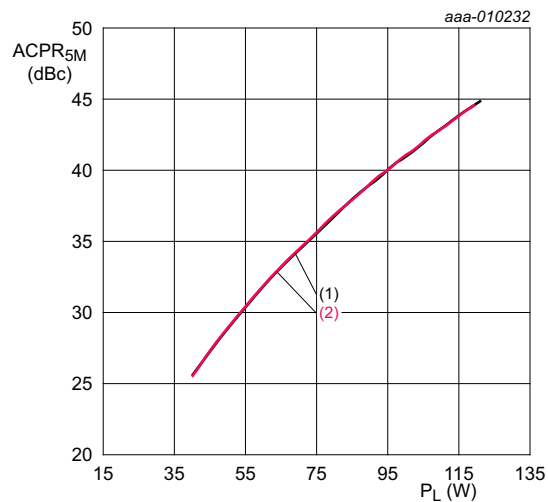
Fig 14. Peak-to-average ratio as a function of output power; typical values

7.5.2.4 2-Carrier W-CDMA



$V_{DS} = 28\text{ V}; I_{Dq} = 2000\text{ mA}$ .  
 (1)  $f = 721\text{ MHz}$   
 (2)  $f = 723\text{ MHz}$

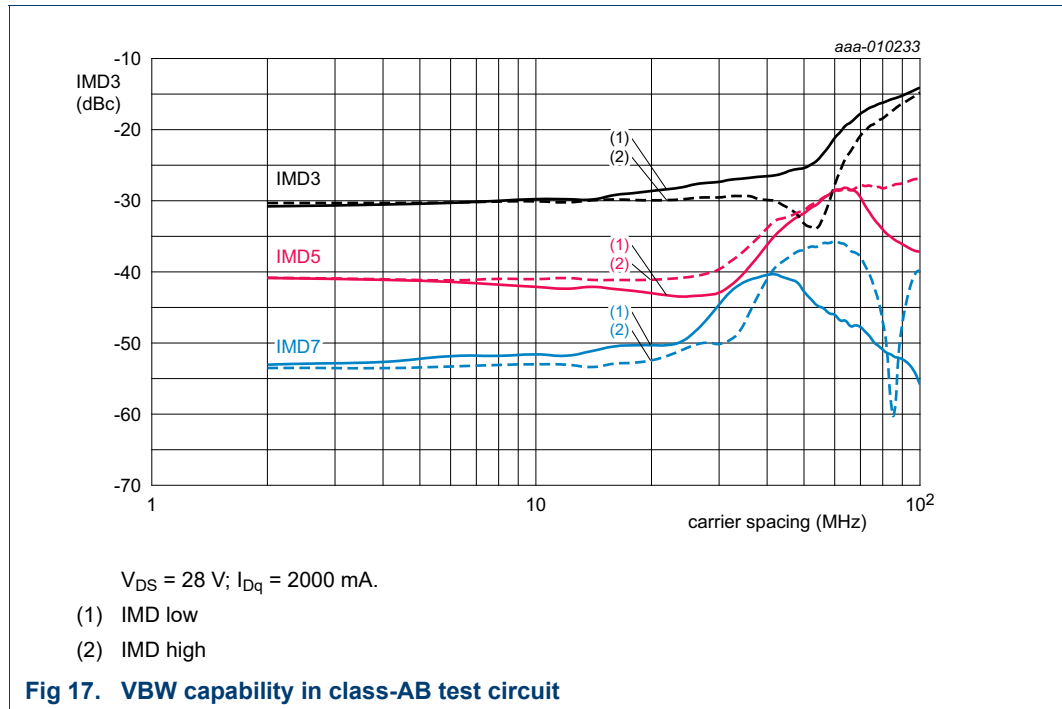
Fig 15. Power gain and drain efficiency as function of output power; typical values



$V_{DS} = 28\text{ V}; I_{Dq} = 2000\text{ mA}$ .  
 (1)  $f = 721\text{ MHz}$   
 (2)  $f = 723\text{ MHz}$

Fig 16. Adjacent channel power ratio (5 MHz) as a function of output power; typical values

7.5.2.5 2-Tone VBW



8. Package outline

Earless flanged ceramic package; 6 leads

SOT1244B

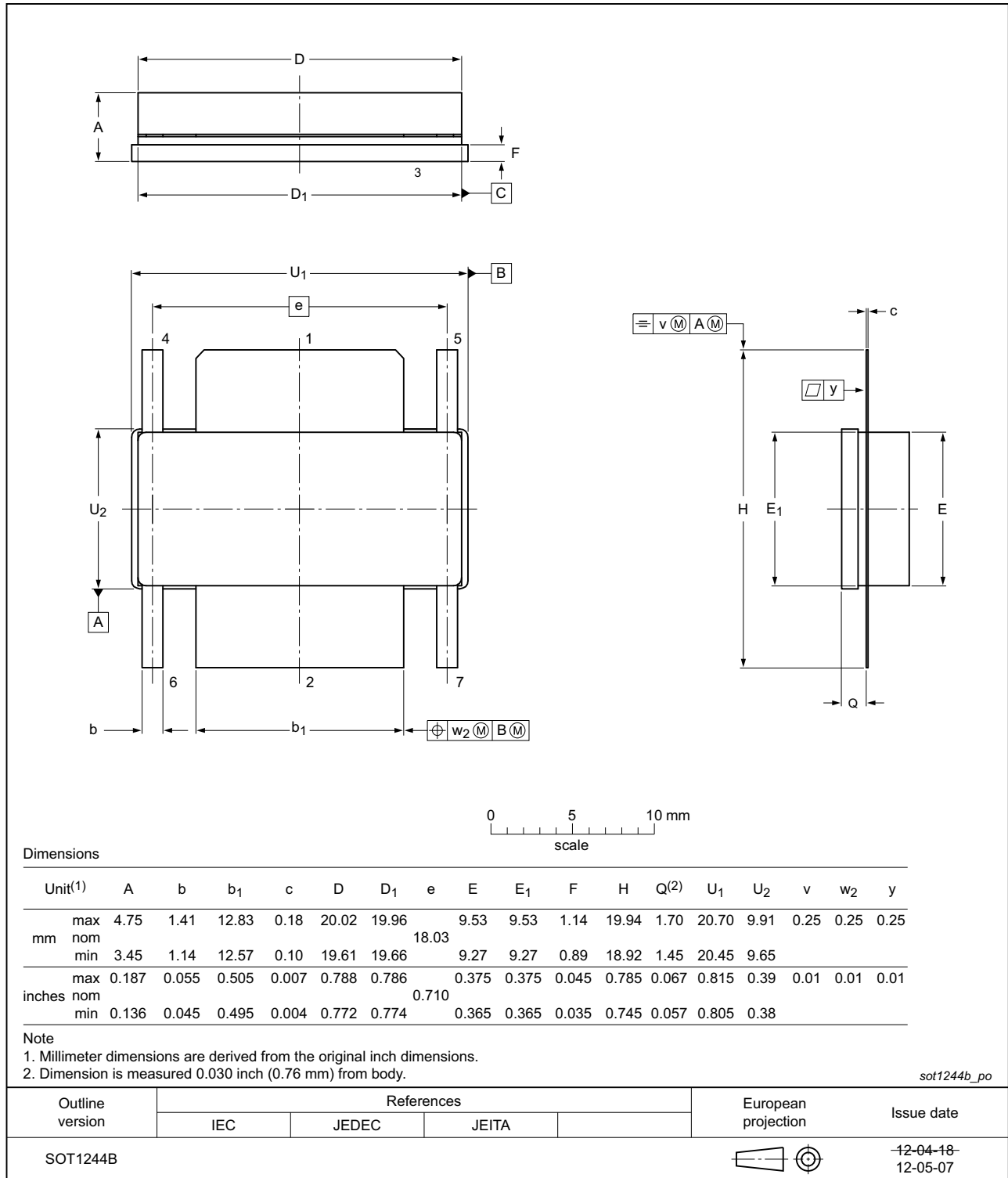


Fig 18. Package outline SOT1244B

Earless flanged ceramic package; 6 leads

SOT1244C

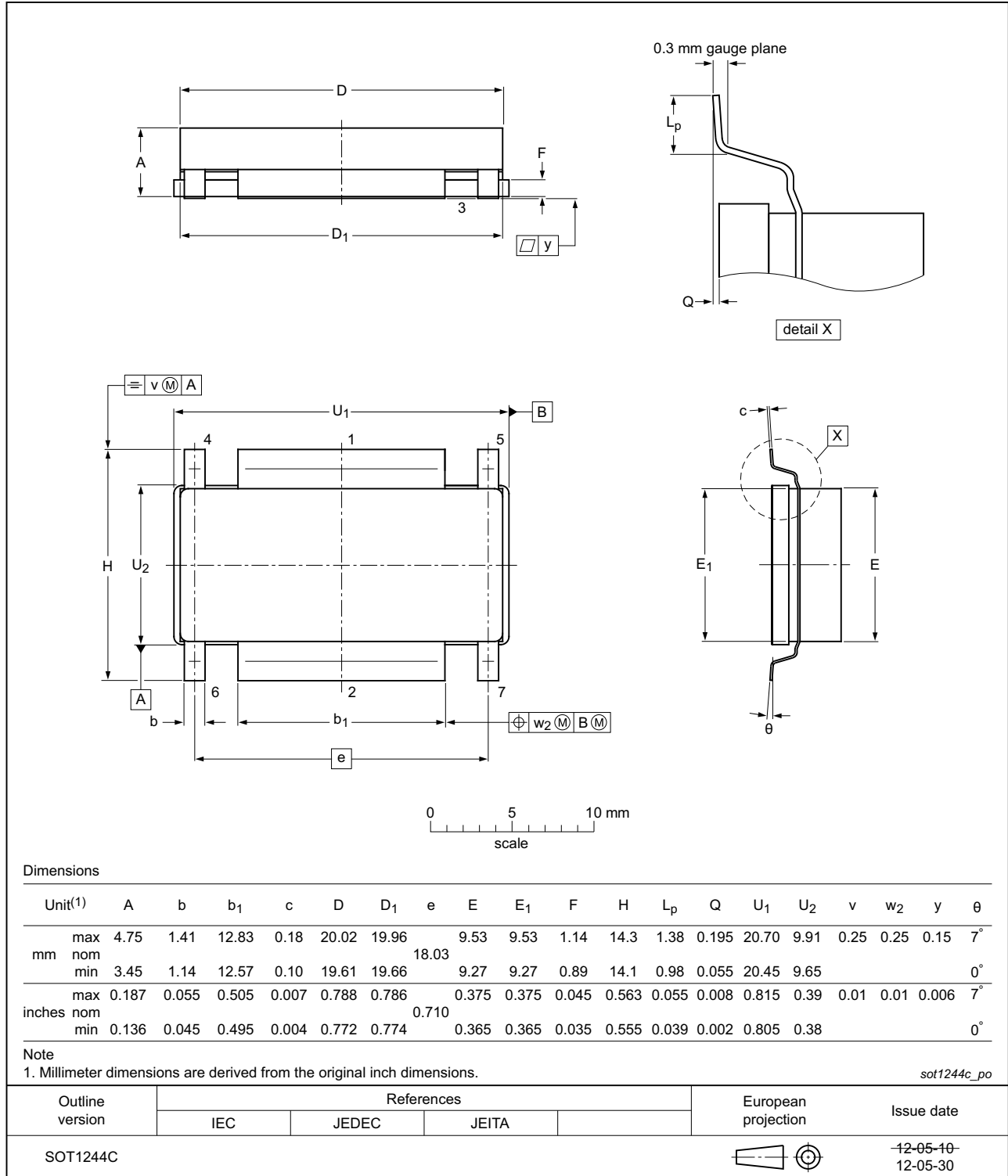


Fig 19. Package outline SOT1244C

## 9. Handling information

### CAUTION



This device is sensitive to ElectroStatic Discharge (ESD). Observe precautions for handling electrostatic sensitive devices.

Such precautions are described in the *ANSI/ESD S20.20*, *IEC/ST 61340-5*, *JESD625-A* or equivalent standards.

## 10. Abbreviations

Table 10. Abbreviations

Acronym	Description
3GPP	3rd Generation Partnership Project
CCDF	Complementary Cumulative Distribution Function
CW	Continuous Wave
DPCH	Dedicated Physical Channel
ESD	ElectroStatic Discharge
LDMOS	Laterally Diffused Metal Oxide Semiconductor
MTF	Median Time to Failure
PAR	Peak-to-Average Ratio
SMD	Surface Mounted Device
VBW	Video Bandwidth
VSWR	Voltage Standing Wave Ratio
W-CDMA	Wideband Code Division Multiple Access

## 11. Revision history

Table 11. Revision history

Document ID	Release date	Data sheet status	Change notice	Supersedes
BLF8G09LS-270W_8G09LS-270GW#3	20150901	Product data sheet	-	BLF8G09LS-270W_8G09LS-270GW v.2
Modifications:	<ul style="list-style-type: none"> <li>The format of this document has been redesigned to comply with the new identity guidelines of Ampleon.</li> <li>Legal texts have been adapted to the new company name where appropriate.</li> </ul>			
BLF8G09LS-270W_8G09LS-270GW v.2	20140117	Product data sheet	-	BLF8G09LS-270W_8G09LS-270GW v.1
BLF8G09LS-270W_8G09LS-270GW v.1	20130927	Objective data sheet	-	-

## 12. Legal information

### 12.1 Data sheet status

Document status <sup>[1][2]</sup>	Product status <sup>[3]</sup>	Definition
Objective [short] data sheet	Development	This document contains data from the objective specification for product development.
Preliminary [short] data sheet	Qualification	This document contains data from the preliminary specification.
Product [short] data sheet	Production	This document contains the product specification.

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