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LatticeXP2 Advanced Evaluation Board

The LatticeXP2 Advanced Evaluation Board is a flexible platform designed to help you quickly evaluate the features and performance of the LatticeXP2 FPGA, or aid in development of custom designs. For more PCB options, see the **LatticeXP2 Standard Evaluation Board**.

Product Contents

Each LatticeXP2 Advanced Evaluation Board includes the following:

LatticeXP2 Advanced Evaluation Board featuring LatticeXP2-17 FPGA in 484fpBGA package (LFXP2-17E-6F484C) DDR2 SDRAM SO-DIMM socket (32-bit) Tri-speed Ethernet PHY (10/100/1G) / RJ45. PCI 32-bit edge LVDS Video Tx/Rx (via MDR-26 connectors) PS/2 I/O Compact Flash connector SMAs for clock and general purpose I/O A/D converter (Burr Brown ADS7842) D/A converter (Burr Brown DAC7617) USB host/controller Built-in USB download capability (includes MachXO device) RS-232 connector (female) on-board oscillator (dip socket) SPI flash memory for alternate configuration LCD connector 4 push-button switches 7-segment LED 8-bit switch 8 general purpose LEDs PAC1220AT8 for on-board power control

USB Cable for download (Note: this board has a Lattice download cable built-in, so only a standard USB cable is required for programming)

Wall Adapter Power Supply (Universal voltage input, US plugs)

Device Support

You will need to following software to use this board:

ispLEVER for design, fitting, place & route of Lattice programmable devices

 ispVM to download your program to the LatticeXP2 device

7:1 LVDS Video Demo & Kit

The Lattice 7:1 Video Demo Kit

is a set of boards and cables that demonstrate the implementation of a 7:1 LVDS solution. This kit is intended to work with the

7:1 LVDS Video Interface Reference Design

. Follow the links to learn more, or download the reference design and demo.



click for enlarged view

Ordering Information

LFXP2-17E-H-EVN To purchase, visit our **online store**.

Note: LFXP2-17E-H-EVN replaces LFXP2-17E-H-EV, which was not RoHS Compliant.